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Serial No:

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Filing Date:

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Art Unit:

2183

Examiner:

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For:

SYSTEM ON A CHIP WITH MULTIPLE POWER PLANES AND

ASSOCIATE POWER MANAGEMENT METHODS

RECEIVED

Hon. Commissioner of Patents Washington, D. C. 20231.

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Technology Center 2100

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Date

REQUEST FOR STATUS OF APPLICATION

Applicants hereby request the current status of the above-identified application for patent. While several papers have been filed with the U.S. Patent and Trademark Office, the last paper received from the U.S. Patent and Trademark Office was the official recordal of assignment documents mailed October 31,2001. No further correspondence has been received.

It is our understanding that the U.S. Patent and Trademark Office has a policy of responding to Requests for Status within thirty (30) days of receipt. Accordingly, please respond prior to October 20, 2002.

If the Examiner has any questions or comments concerning this paper or the present parent and/or divisional application in general, the Examiner is invited to call the undersigned at (214) 745-5374.

Respectfully submitted, WINSTEAD SECHREST & MINICK Attorneys for Applicant

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Attorney's Docket: 2836- P139US

Date Sept. 13, 2002

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